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SB/08A (08-03) 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Complete if Known	
Application Number	10/562,989
Filing Date	Dec. 28, 2005
First Named Inventor	Jurgen Ficker
Group Art Unit	~~Not Assigned~~
Examiner Name	~~Not Assigned~~
Attorney Docket Number	411000-143

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Group Art Unit	~~Not Assigned~~
Examiner Name	~~Not Assigned~~
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First Named Inventor	Jurgen Ficker
Group Art Unit	~~Not Assigned~~
Examiner Name	~~Not Assigned~~
Attorney Docket Number	411000-143

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Sheet	6	4110 00-143	11

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Application Number	10/562,989
Filing Date	Dec. 28, 2005
First Named Inventor	Jurgen Ficker
Group Art Unit	~~Not Assigned~~
Examiner Name	~~Not Assigned~~
Attorney Docket Number	411000-143

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Complete if Known	
Application Number	10/562,989
Filing Date	Dec. 28, 2005
First Named Inventor	Jurgen Ficker
Group Art Unit	~~Not Assigned~~
Examiner Name	~~Not Assigned~~
Attorney Docket Number	411000-143

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Substitute for form 1449A/PTO				Complete if Known	
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				Group Art Unit	Not Assigned
(Use as many sheets as necessary)				Examiner Name	Not Assigned
Sheet	11	4110 00-143	11	Attorney Docket Number	411000-143

/N.S./		WANG, Hsing Lin et al., "Conducting Polymer Blends: Polythiophene and Polypyrrole Blends with Polystyrene and Poly (bisphenol A carbonate), American Chemical Society, 1990 pp. 1053 – 1059.	X
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Examiner Signature		/Nahida Sultana/	Date Considered 01/21/2009

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹ Applicant's unique citation designation number (optional). ² See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached. This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICATION OF:	Jurgen Ficker et al.		
SERIAL NO:	10/562,989	GROUP ART UNIT:	~~Not assigned~~
FILED:	Dec. 28, 2005	EXAMINER:	~~Not assigned~~
:		CUSTOMER NO.	27162
FOR:	Method and Device for Patterning Organic Layers		
ATTY/DKT NO.:	411000-143		

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

DISCLOSURE STATEMENT UNDER 37 CFR 1.56

SIR:

This paper is to bring to the attention of the PTO the following commonly owned copending U.S. applications, all of which are related in different respects to organic electronic devices and/or method of making such devices such as transistors, diodes, integrated circuits and the like. Many of these applications also have one or more common inventors. The enclosed PTO 1449 lists these applications. It is respectfully requested that the Examiner consider and make of record all of the cited applications listed on the attached PTO 1449.

Application No.	Title	Inventors	Atty. Dkt. No.
/N.S./ 10/332,140	Method for the Production and Configuration of Organic Field-Effect Transistors (OFET)	Adolf Bernds et al.	411000-103
/N.S./ 10/344,951	Organic Field-Effect Transistor (OFET), A Production Method Therefor, An Integrated Circuit Constructed From the	Adolf Bernds et al.	411000-99

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	Same and Their Uses		
/N.S./ 10/362,932	Organic Field Effect Transistor, Method for Structuring an OFET and Integrated Circuit	Adolf Bernds et al	411000-110
10/380,113	Organic Rectifier, Circuit, RFID Tag and Use of an Organic Rectifier	Adolf Bernds et al.	411000-106
10/380,206	Organic Memory, Identification Marker (RFID-TAG) with Organic Memory and Uses of an Organic Memory	Adolf Bernds et al.	411000-102
10/381,032	Electrode and/or Conductor Track for Organic Components and Production Method Thereof	Adolf Bernds et al.	411000-105
10/433,959	Organic Field Effect Transistor, Method For Structuring an OFET and Integrated Circuit	Adolf Bernds	411000-108
10/433,961	Device For Detecting and/or Transmitting at Least One Environmental Influence, Method for Producing Said Device and Use Thereof	Wolfgang Clemens et al.	411000-111
10/467,636	Organic Field Effect Transistor With a Photostructured Gate Dielectric, Method for the Production and Use Thereof in Organic Electronics	Adolf Bernds et al.	411000-104
10/473,050	Device With At Least Two Organic Electronic Components and Method for Producing the Same	Adolf Bernds et al.	411000-113
10/479,234	Organic Field Effect Transistor, Method for Production and Use Thereof in the Assembly of Integrated Circuits	Adolf Bernds et al.	411000-101
10/479,238	Method For Producing Conductive Structures by Means of Printing Technique, and Active Components Produced Therefrom For Integrated Circuits	Adolf Bernds et al.	411000-100
10/492,922	Insulator for An Organic Electronic Component	Erwann Guillet et al.	411000-115
10/492,923	Electronic Unit, Circuit Design for the Same and Production Method	Wolfgang Clemens et al.	411000-114
10/498,610	Organic Field Effect Transistor with Offset Threshold Voltage and the Use Thereof	Walter Fix et al.	411000-119
10/508,640	Logic Component Comprising Organic Field Effect Transistors	Walter Fix et al.	411000-120

/N.S./ 10/508,737	Device and Method for Laser Structuring Functional Polymers and	Adolf Bernds et al.	411000-121
/N.S./ 10/517,750	Substrate for an Organic Field Effect Transistor, Use of the Substrate, Method	Wolfgang Clemens et al.	411000-122

01/21/2009

/Nahida Sultana/

/N.S./

	of Increasing the Charge Carrier Mobility and Organic Field Effect Transistor (OFET)		
10/523,216	Electronic Component Comprising Predominantly Organic Functional Materials And A Process For The Production Thereof	Adolf Bernds et al.	411000-123
10/523,487	Electronic Device	Wolfgang Clemens et al.	411000-124
10/524,646	Organic Component for Overvoltage Protection and Associated Circuit	Walter Fix et al.	411000-127
10/533,756	Organic Electronic Component with High-Resolution Structuring and Process for the Production Thereof	Wolfgang Clemens et al.	411000-128
10/534,678	Measuring Apparatus for Determining an Analyte in a Liquid Sample	Wolfgang Clemens et al.	411000-129
10/535,448	Organic Electronic Component Comprising Semi-Conductive Functional Layer and Method for Producing Said Component	Wolfgang Clemens et al.	411000-131
10/535,449	Organic Electronic Component Comprising the Same Organic Material for at Least Two Functional Layers	Adolf Bernds et al.	411000-132
10/344,926	An Electronic Circuit Having an Encapsulated Organic-Electronic Component, and a Method for Making an Encapsulated Organic-Electronic Component	Wolfgang Clemens et al.	411000-133
10/541,815	Organo-Resistive Memory Unit	Axel Gerlt et al.	411000-136
10/541,956	Board or Substrate for an Organic Electronic Device and Use Thereof	Wolfgang Clemens et al.	411000-137
10/541,957	Organic Field Effect Transistor And Integrated Circuit	Walter Fix et al.	411000-138
10/543,561	Organic Storage Component and Corresponding Triggering Circuit	Wolfgang Clemens et al.	411000-139
10/542,678	Organic Electronic Component and Method For Producing Organic Electronic Devices	Adolf Bernds et al.	411000-140
10/542,679	Use of Conductive Carbon Black/Graphite Mixtures for the Production of Low-Cost Electronics	Adolf Bernds et al.	411000-141
10/562,989	Method and Device for Patterning Organic Layers	Jurgen Ficker et al.	411000-143
10/562,869	Logic Gate with a Potential-free Gate Electrode for Organic Integrated Circuits	Wolfram Glauert et al.	411000-144

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet 1 2

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Application Number	10/562,989
Filing Date	**Dec. 28, 2005**
First Named Inventor	Jurgen Ficker
Group Art Unit	~~"Not assigned"~~
Examiner Name	~~Not assigned~~
Attorney Docket Number	411000-143

U.S. PATENT DOCUMENTS

Examiner Initial*	Cite No.[1]	Document Number Number-Kid Code[2] (if known)	Publication- Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
/N.S./	103	US-6,852,583	10/09/2003	Adolf Bernds et al.	See accompanying Disclosure Statement filed herewith
	102	US-6,903,958	03/21/2002	Adolf Bernds et al.	
	133	US-10/344,926	02/12/2004	Adolf Bernds et al.	
	99	US-10/344,951	02/12/2004	Adolf Bernds et al.	
	110	US-10./362,932	10/02/2003	Adolf Bernds et al.	
	106	US-10/380,113	09/25/2003	Adolf Bernds et al.	
	105	US-10/381,032	02/12/2004	Adolf Bernds et al.	
	108	US-10/433,959	04/01/2004	Adolf Bernds	
	111	US-10/433,961	04/01/2004	Wolfgang Clemens et al.	
	109	US-10/451,108	05/13/2004	Mark Giles et al.	
	104	US-10/467,636	11/04/2004	Adolf Bernds et al.	
	113	US-10/473,050	05/20/2004	Adolf Bernds et al.	
	101	US-10/479,234	12/30/2004	Adolf Bernds et al.	
	100	US-10/479,238	10/20/2004	Adolf Bernds et al.	
	115	US-10/492,922	03/03/2005	Erwann Buillet et al.	
	114	US-10/492,923	12/23/2004	Wolfgang Clemens et al.	
	119	US-10/498,610	N/A	Walter Fix et al.	
	120	US-10/508,640	N/A	Walter Fix et al.	
	121	US-10/508,737	N/A	Adolf Bernds et al.	
	122	US-10/517,750	N/A	Wolfgang Clemens et al.	
	123	US-10/523,216	N/A	Adolf Bernds et al.	
	124	US-10/523,487	N/A	Wolfgang Clemens et al.	
	127	US-10/524,646	N/A	Walter Fix et al.	
	128	US-10/533,756	N/A	Wolfgang Clemens et al.	

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Sheet 2 2

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First Named Inventor	Jurgen Ficker
Group Art Unit	~~Not assigned~~
Examiner Name	Not assigned
Attorney Docket Number	411000-143

/N.S./	129	US-10/534,678	N/A	Wolfgang Clemens et al.	
	131	10/535,448	N/A	W. Clemens et al.	
	132	10/535,449	N/A	Walter Fix et al.	
	136	US-10/541,815	N/A	Axel Gerlt et al.	
	137	US-10/541,956	N/A	Wolfgang Clemens et al.	
	138	US10/541,957	N/A	Walter Fix et al.	
	139	US-10/543,561	N/A	Wolfgang Clemens et al.	
	140	US-10/542,678	N/A	Adolf Bernds et al.	
	141	US-10/542,679	N/A	Adolf Bernds et al.	
	143	US-10/562,989	N/A	Jurgen Ficker et al.	
V	144	US-10/562,869	N/A	Wolfram Glauert et al.	

Examiner Signature	/Nahida Sultana/	Date Considered	01/21/2009

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